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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/760,661	
	Filing Date	January 15, 2004	
	First Named Inventor	Mark L. Jones	
	Art Unit	1743	
	Examiner Name		
Total Number of Pages in This Submission	28	Attorney Docket Number	WSC 303

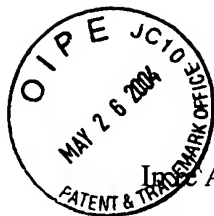
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Peter D. Sabido Kolisch Hartwell, P.C.
Signature	
Date	May 24, 2004

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor Application of:

MARK L. JONES, ANSGAR KOCH,
FUMITOSHI ITO and JAMES J. SHEN

Dated: May 24, 2004

Serial No. : 10/760,661

Our File: WSC 303

Filing Date : January 15, 2004

Group Art Unit: 1743

For: : METHOD OF ANALYZING A WAFER FOR METAL IMPURITIES

MAIL STOP: AMENDMENT

Commissioner for Patents

P.O. Box 1450

Alexandria, Virginia 22313-1450

Sir:

INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. §§ 1.56, 1.97, and 1.98

Applicants are submitting this Information Disclosure Statement pursuant to 37 C.F.R. §§ 1.56, 1.97, and 1.98 to disclose to the U.S. Patent and Trademark Office the patents, publications, applications, and/or other references listed on the enclosed, completed PTO-1449 form(s). The filing of this Information Disclosure Statement should not be construed as a representation that a search has been made or as an admission that the listed references are prior art for this application. Applicants respectfully request that the listed references be expressly considered during prosecution of the application, and that the references be made of record therein and appear among the 'references cited' on any patents issuing therefrom.

CONTENT OF DISCLOSURE

This Information Disclosure Statement includes (1) one page of PTO-1449 forms, and (2) a legible copy of each foreign and non-patent reference, if any, listed on the form(s). However, because this application was filed on or after July 1, 2003, no copies of U.S. patents or published U.S. patent applications are included.

TIMING OF DISCLOSURE / FEE INFORMATION

This Information Disclosure Statement is being filed, to the best of the undersigned's knowledge, either (1) before the mailing of a first Office action on the merits, or (2) before the mailing of a first Office action after the filing of a request for continued examination under 37 C.F.R. § 1.114. Therefore, in accordance with 37 C.F.R. § 1.97(b), no fee or statement under 37 C.F.R. § 1.97(e) is required.

Please contact the undersigned with any questions or comments regarding this Information Disclosure Statement.

Respectfully submitted,

KOLISCH HARTWELL, P.C.



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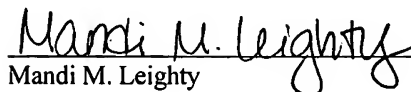
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Mandi M. Leighty

SHEET 1 OF 1

FORM PTO-1449		DOCKET NUMBER WSC 303		APPLICATION NUMBER 10/760,661		
INFORMATION DISCLOSURE CITATION IN AN APPLICATION		APPLICANT(S) Mark L. Jones et al.				
		FILING DATE January 15, 2004		GROUP ART UNIT 1743		
U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FIL. DATE IF APPROP.
	5,635,410	6/3/1997	Kusuda			
	5,804,981	9/8/1998	Lowell et al.			
	5,943,552	8/24/1999	Koveshnikov et al.			
	6,136,669	10/24/2000	Flitsch et al.			
	6,140,131	10/31/2000	Sunakawa et al.			
	6,423,615 B1	7/23/2002	Ravi et al.			
	6,524,928 B1	2/25/2003	Hirabayashi			
FOREIGN PATENT DOCUMENTS						
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES NO
OTHER DOCUMENTS						
✓	M. B. Shabani, <i>Kinetic of Low-Temperature Out-Diffusion of Cu from Silicon Wafers</i> , Abstract No. 1389, submitted to ECS in Oct. 1999					
✓	Shabani, M., and Okuuchi, S., <i>Effect of Dopants and Oxygen Precipitation on Low-Temperature Out-Diffusion and Gettering of Cu in Silicon Wafer</i> , Proceedings of Seventh International Symposium on Silicon-on-Insulator Technology and Devices, published by The Electrochemical Society, 96-3, 10 (1997)					
✓	C.M. McCarthy et al., <i>Room Temperature Copper Out-Diffusion from the Bulk of Silicon Wafers and its Behavior Following Various Postanneal Treatments</i> , ECS Proc. 98-1, 629-640 (1998)					
EXAMINER			DATE CONSIDERED			